

## INFORMATION DISCLOSURE STATEMENT

Applicant : DiBene, et al.  
App. No. : 10/036,957  
Filed : December 20, 2001  
For : ULTRA-LOW IMPEDANCE POWER  
INTERCONNECTION SYSTEM FOR  
ELECTRONIC PACKAGES  
Examiner : Dinh, T.  
Group Art Unit : 2827

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Enclosed is form PTO-1449 listing 20 references that are also enclosed.

This Information Disclosure Statement is being filed under 37 C.F.R. § 1.97(c)(2) before the mailing date of a final action and before the mailing of a Notice of Allowance. This Statement is accompanied by the fees set forth in 37 C.F.R. § 1.17(p). The Commissioner is hereby authorized to charge any additional fees which may be required or to credit any overpayment to Account No. 11-1410.

Respectfully submitted,

KNOBBE, MARTENS, OLSON & BEAR, LLP

Dated: Dec 12, 2003

By: Raymond B. Hom

Raymond B. Hom  
Registration No. 44,773  
Attorney of Record  
Customer No. 20,995  
(619) 235-8550

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FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.  
INCEP.017AAPPLICATION NO.  
10/036957INFORMATION DISCLOSURE STATEMENT  
BY APPLICANT

(USE SEVERAL SHEETS IF NECESSARY)

APPLICANT  
DiBene II, et al.FILING DATE  
December 20, 2001GROUP  
2835

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
	1	5,413,489	05/09/95	Switky, Andrew			
	2	5,770,891	06/23/98	Frankeny, et al.			
	3	6,226,179	05/01/01	Lee, Sung-Bae			

## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	4	EP 0 582 145 A1	02/09/94	EPO				
	5	EP 0 910 235 A1	04/21/99	EPO				
	6	EP 0 920 055 A2	06/02/99	EPO				
	7	JP 03041753	02/22/91	Japan				
	8	JP 08204304	08/09/96	Japan				
	9	JP 08330699	12/13/96	Japan				
	10	JP 1174427	07/11/89	Japan				

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)	
	11	Chung, "Ball-Grid-Array Package Thermal Management," 14th IEEE Semi-therm TM Symposium, 1998, 78-87
	12	XP000124263, IBM Tech Disc Bulletin, "Multiple Electronic Board.... System," 1994, 33(3B):55-56
	13	IBM Corp. Technical Disclosure Bulletin, "Pin Fin Array Heat Pipe Apparatus," September 1994, Vol. 37, No. 9, 1pp.

EXAMINER

DATE CONSIDERED

\*EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED, INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.

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## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	14	JP 57066654	04/22/82	Japan				
	15	JP 58175851	10/15/83	Japan				
	16	WO 01/65344	09/07/01	PCT				
	17	WO 01/67512	09/13/01	PCT				
	18	WO 01/06821 A1	01/25/01	PCT				
	19	WO 01/33927 A1	05/10/01	PCT				
	20	WO 96/23397	08/01/96	PCT				

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)	

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EXAMINER	DATE CONSIDERED
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